

# 2 W Flange Ceramic Packaged PHEMT GaAs Power FETs

#### **FEATURES**

- 2 W Typical Output Power at 6 GHz
- 10 dB Typical Linear Power Gain at 6 GHz
- High Linearity: IP3 = 43 dBm Typical at 6 GHz
- High Power Added Efficiency:
  Nominal PAE of 43 % at 6 GHz
- Suitable for High Reliability Application
- Breakdown Voltage: BV<sub>DGO</sub> ≥ 18 V
- $Lg = 0.6 \mu m$ , Wg = 5 mm
- Tight Vp ranges control
- High RF input power handling capability
- 100 % DC Tested
- Flange Ceramic Package

#### PHOTO ENLARGEMENT



## **DESCRIPTION**

The TC2696 is packaged with the TC1606 Pseudomorphic High Electron Mobility Transistor (PHEMT) chip. The flange ceramic package provides the best thermal conductivity for the GaAs FET. All devices are 100% DC and RF tested to assure consistent quality. Typical applications include high dynamic range power amplifiers for commercial and military high performance power applications.

## **ELECTRICAL SPECIFICATIONS (T<sub>A</sub>=25 °C)**

EBECTRICIE SI ECH TCHTTONS (1A-25 °C)						
Symbol	CONDITIONS	MIN	TYP	MAX	UNIT	
$P_{1dB}$	Output Power at 1dB Gain Compression Point, $f = 6$ GHz $V_{DS} = 8$ V, $I_{DS} = 500$ mA	32.5	33		dBm	
$G_{L}$	Linear Power Gain, $f = 6$ GHz $V_{DS} = 8$ V, $I_{DS} = 500$ mA		10		dB	
IP3	Intercept Point of the $3^{\text{rd}}$ -order Intermodulation, $f = 6 \text{ GHz V}_{DS} = 8 \text{ V}$ , $I_{DS} = 500 \text{ mA}$ , $*P_{SCL} = 20 \text{ dBm}$		43		dBm	
PAE	Power Added Efficiency at 1dB Compression Power, $f = 6$ GHz		43		%	
$I_{DSS}$	Saturated Drain-Source Current at $V_{DS} = 2 \text{ V}$ , $V_{GS} = 0 \text{ V}$		1.25		A	
$g_{\rm m}$	Transconductance at $V_{DS} = 2 \text{ V}$ , $V_{GS} = 0 \text{ V}$		850		mS	
$V_P$	Pinch-off Voltage at $V_{DS} = 2 \text{ V}$ , $I_D = 10 \text{ mA}$		-1.7**		Volts	
$BV_{DGO}$	Drain-Gate Breakdown Voltage at I <sub>DGO</sub> =2.5 mA	15	18		Volts	
R <sub>th</sub>	Thermal Resistance	•	7		°C/W	

### Note: \* P<sub>SCL</sub>: Output Power of Single Carrier Level.

\*\* For the tight control of the pinch-off voltage range, we divide TC2696 into 3 model numbers to fit customer design requirement (1)TC2696P1519: Vp = -1.5V to -1.9V (2)TC2696P1620: Vp = -1.6V to -2.0V (3)TC2696P1721: Vp = -1.7V to -2.1V If required, customer can specify the requirement in purchasing document. For special Vp requirement, please contact factory for details.

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PFV4	20070507
\C V 4_	_20070307

# **ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub>=25 °C)**

Symbol	Parameter	Rating
$V_{\mathrm{DS}}$	Drain-Source Voltage	12 V
$V_{GS}$	Gate-Source Voltage	-5 V
$I_{DS}$	Drain Current	$I_{DSS}$
P <sub>in</sub>	RF Input Power, CW	30 dBm
$P_{T}$	Continuous Dissipation	7.7 W
$T_{CH}$	Channel Temperature	175 °C
$T_{STG}$	Storage Temperature	- 65 °C to +175 °C

## RECOMMANDED OPERATING CONDITION

Symbol	Parameter	Rating		
$V_{\mathrm{DS}}$	Drain to Source Voltage	8 V		
$I_{\mathrm{D}}$	Drain Current	500 mA		

#### HANDLING PRECAUTIONS:

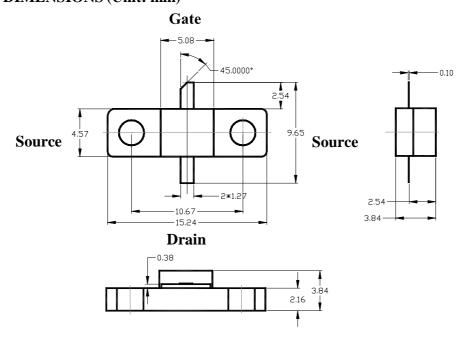
The user must operate in a clean, dry environment. Electrostatic Discharge (ESD) precautions should be observed at all stages of storage, handling, assembly, and testing. The static discharge must be less than 300V.

# TYPICAL SCATTERING PARAMETERS (T<sub>A</sub>=25 °C)

 $V_{DS} = 8 \text{ V}, I_{DS} = 500 \text{ mA}$ 

FREQUENCY	S11		S21		S12		S22	
(GHz)	MAG	ANG	MAG	ANG	MAG	ANG	MAG	ANG
2	0.9232	175.28	1.8019	58.43	0.0288	-14.46	0.6268	174.83
3	0.9179	161.34	1.2279	38.12	0.0303	-26.85	0.6531	167.56
4	0.9098	148.40	0.9600	19.07	0.0327	-38.61	0.6737	158.81
5	0.8978	134.59	0.8236	0.11	0.0364	-50.96	0.6847	148.28
6	0.8806	118.62	0.7599	-19.79	0.0420	-64.88	0.6839	135.52
7	0.8574	99.05	0.7426	-41.79	0.0496	-81.49	0.6699	119.60
8	0.8295	74.18	0.7529	-67.22	0.0595	-102.04	0.6430	98.92
9	0.8045	42.52	0.7648	-97.22	0.0701	-127.59	0.6085	71.06

## **OUTLINE DIMENSIONS (Unit: mm)**



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