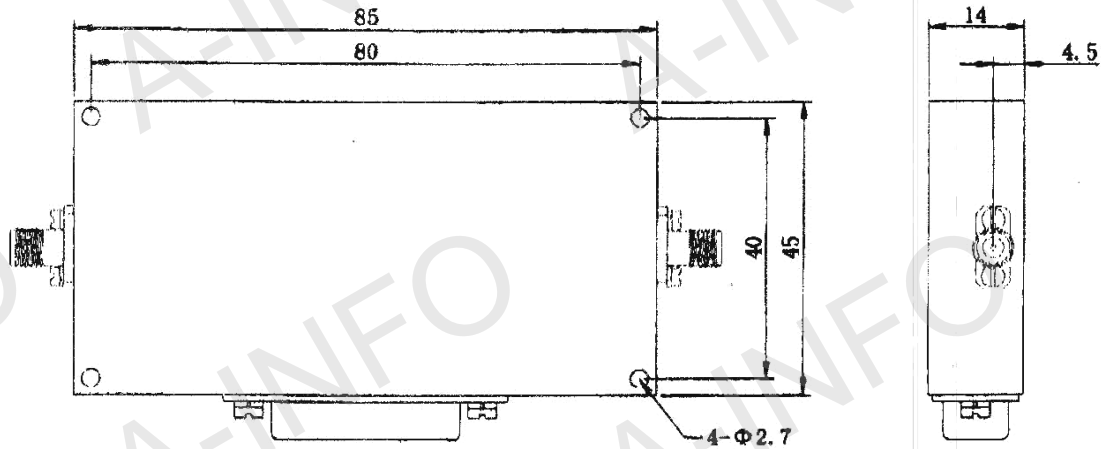


Outline Drawing(Size: mm)



PIN Definition:

PIN No.	1	2	3	4	5	6	7	8	9-12	13	14	15
PIN Definition	C1	C2	C3	C4	C5	C6	C7	C8	N/A	+5V	GND	-5V
Control Logic	1.4°	2.8°	5.6°	11.25°	22.5°	45°	90°	180°	N/A	+5V	GND	-5V

TTL Control

TTL High Level = "1", TTL Low Level = "0"

Technical Specification

Item	Technical Specification
Frequency Range(MHz)	1315 - 1515
Insertion Loss(dB)	4±1 over frequency and phase range
VSWR	1.5 max
RF Input Power Operating(dBm)	+20 Peak or CW
Phase Shift(°)	0-360 minimum over frequency
Phase Shift Flatness(°)	±2% maximum over frequency of the applied phase
Phase accuracy at center frequency(°)	±2.5% of the applied phase
Phase Temperature Stability(°)	no more than ±5 or ±5% whichever is greater over the specified temperature range
TTL Control Compatible (V)	3
Phase Control(°)	8 bit 1.4 LSB
Amplitude Ripple (dB)	±0.5 max over frequency and phase range
Switching Time (ns)	300 max
DC Bias Available (V)	5 - 5
Phase aperture between each phase shifter over frequency range (°)	±3

Connectors	RF in/out SMA female
	DC and control D-SUB 15 pin
Phase Aperture Between Each Phase Shifter Over Frequency Range (°)	±2.5 objective
Storage Temperature (°C)	-20 - +60
Operating Temperature (°C)	0 - 50
Finish	Painting Grey

Test Instruments

- VNA (HP8722D)

Test Condition

- DC Supply: ±5V
- IF Bandwidth: 3000Hz
- Input Power: -10dBm

Test Results

